

Figure 1

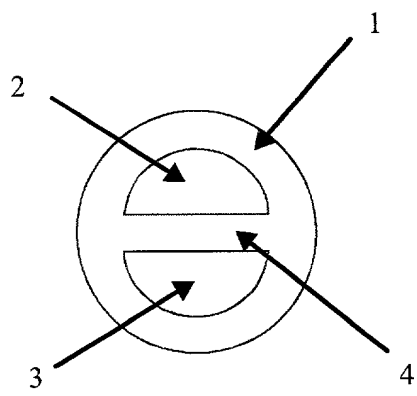


Figure 2

Figure 3

Figure 4 is a schematic diagram of a cylindrical device, likely a microfluidic chip or a small-scale reactor. It consists of two parallel horizontal channels. The top channel is labeled '1' and the bottom channel is labeled '3'. Both channels have a shaded, textured interior. On the left side, there are two inlet ports: the top one is labeled '2' and the bottom one is labeled '3'. On the right side, there is a single outlet port labeled '5'. The device is shown in a perspective view, with the top and bottom surfaces visible.

Figure 4

09973637.100001

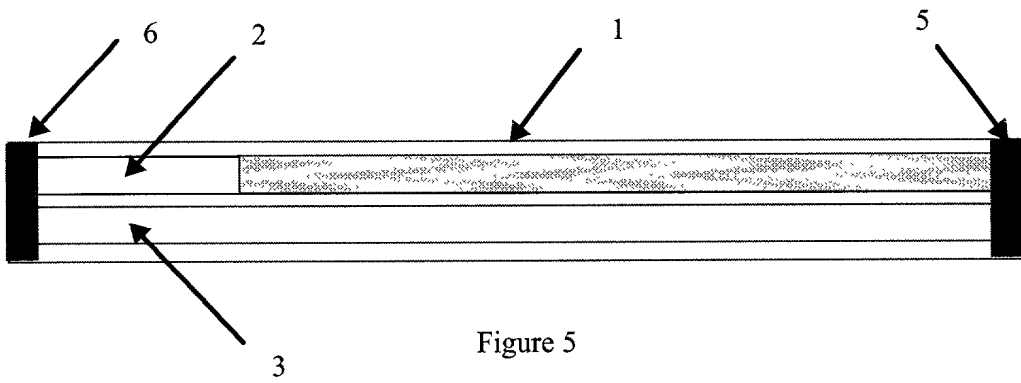


Figure 5

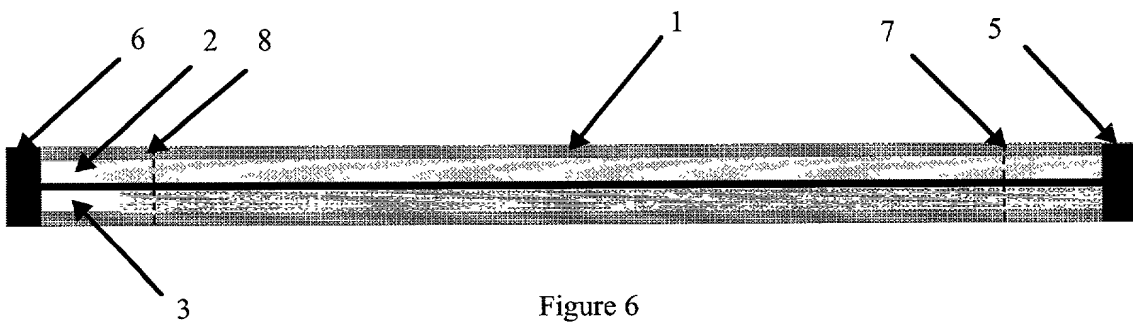


Figure 6

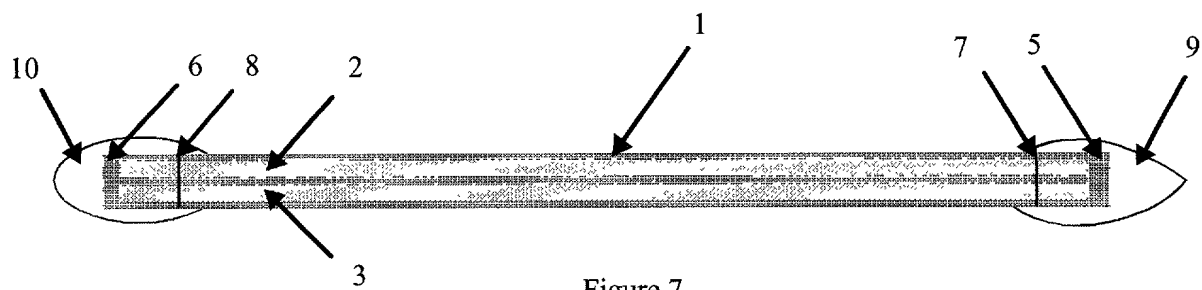


Figure 7

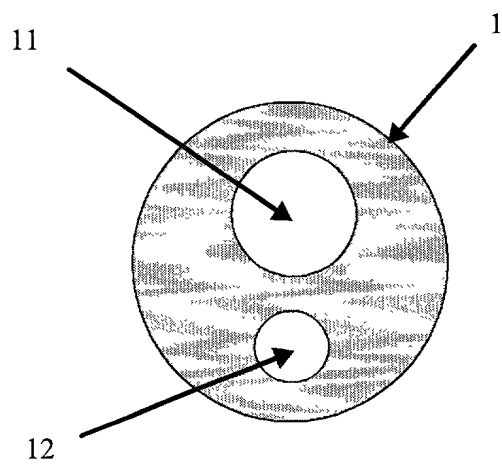


Figure 8

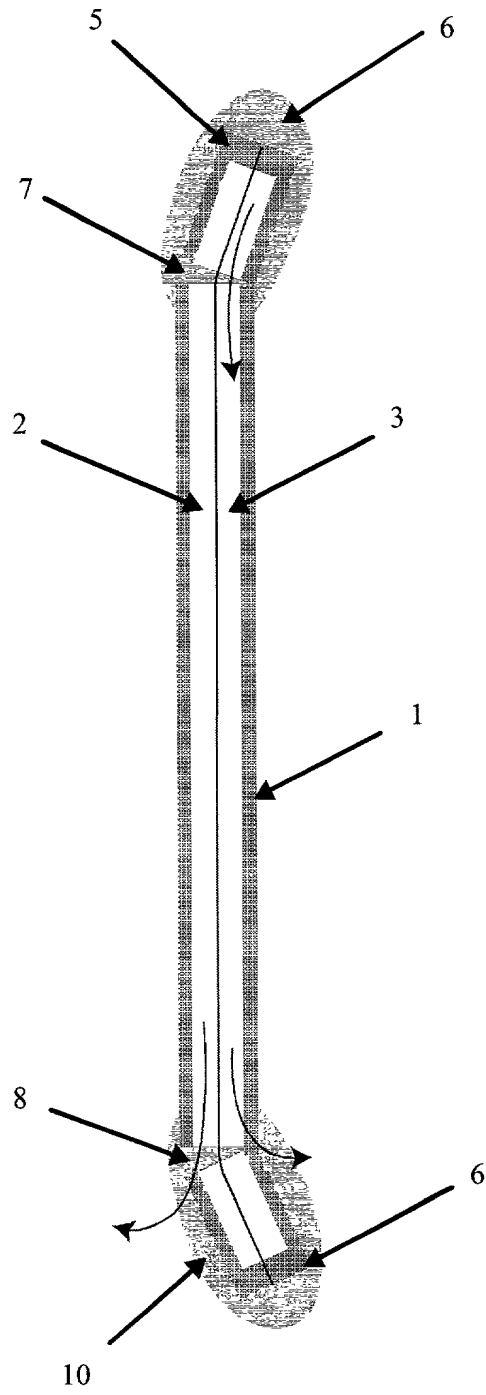


Figure 9

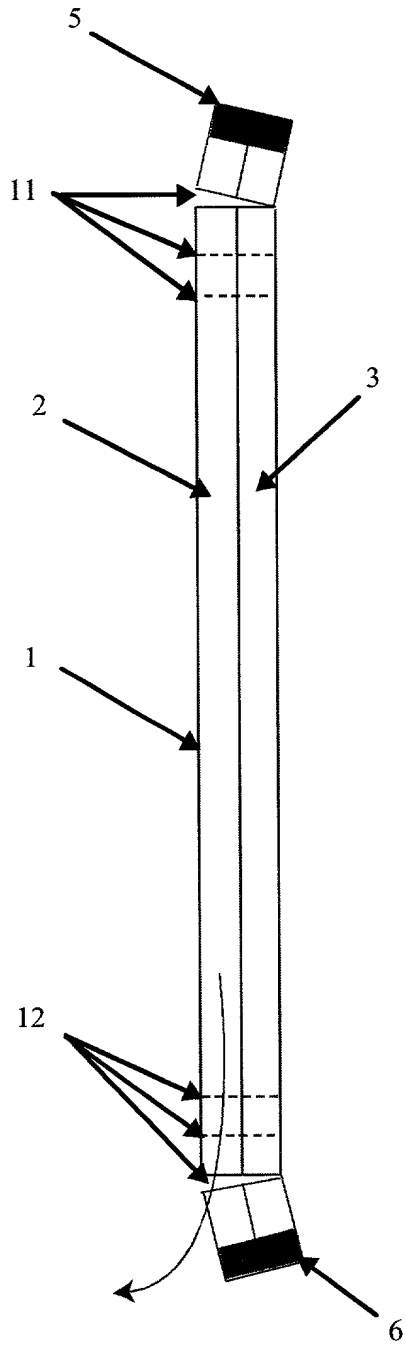


Figure 10

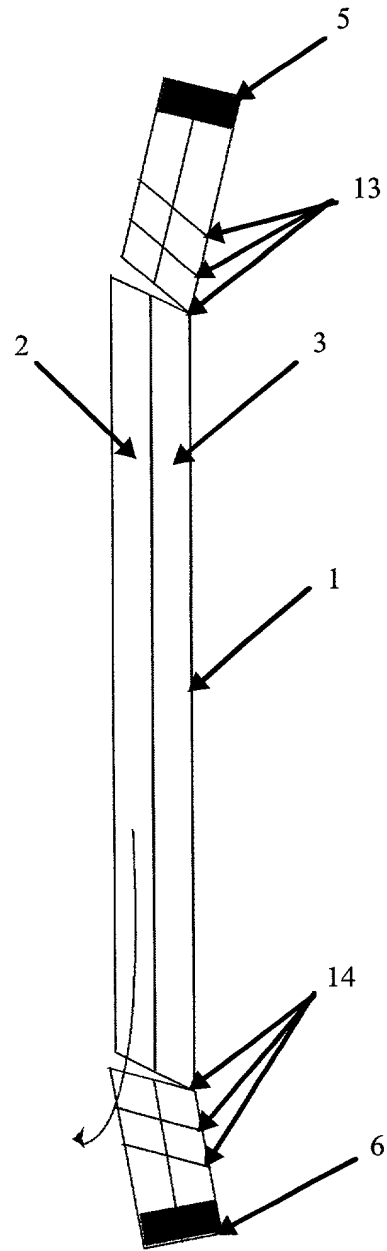


Figure 11